

FORM PTO 1449 (modified)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)				ATTY DOCKET NO. <b>NL030715US1</b>		APPLICATION NO. <b>10/560,677</b>	
				APPLICANT <b>Roger Cuppens, et al.</b>			
				FILING DATE <b>December 14, 2005</b>			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,541,073	9-10-1985	Brice et al.			
		4,635,229	1-6-1987	Okumura et al.			
		5,602,776	2-11-1997	Herd et al.			
		5,646,885	7-8-1997	Matsuo et al.			
		5,696,455	12-9-1997	Madurawe			
		5,805,496	9-8-1998	Batson et al.			
		5,914,895	6-22-1999	Jenne			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
		58-94227 A	6-4-1983	JP			see US 4,541,073
		60-151898 A	8-9-1985	JP			w/ Engl. abst; see US 4,635,229
		62-33393 A	2-13-1987	JP			w/ Engl. abst.
		3-97197 A	4-23-1997	JP			w/ Engl. abst.
		95/22144 A1	8-17-1995	WO			English original
		7-226088 A	8-22-1995	JP			w/ Engl. abst. & machine Engl. transln.
		8-509091 A	2-20-1996	JP			see WO 95/22144
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)							
		Chrzanowska-Jeske, "Architecture and Technology of FPGAs-an Overview", Northcon/93, 10/12-14/1993, pgs. 82-86					
		Notification of Reason For Rejection in JP 2006-516674 (April 28, 2009)					
		Office Action in CN 200480016966.9					
EXAMINER				DATE CONSIDERED			
/Han Yang/				11/20/2009			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /H.Y./

